



Docket No.: M4065.0590/P590-A
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Howard E. Rhodes et al.

Allowed: November 24, 2004

Application No.: 10/632,916

Confirmation No.: 4917

Filed: August 4, 2003

Art Unit: 2813

For: CMOS IMAGER AND METHOD OF
FORMATION

Examiner: C. P. Chaudhari

**COMMENTS ON EXAMINER'S
STATEMENT OF REASONS FOR ALLOWANCE**

Mail Stop Issue Fee
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Applicants agree in part with the Examiner's statement for allowance in that the prior art of record fails to disclose the limitations noted by the Examiner. However, the Examiner's statement does not reflect all of the language of each of the allowed claims. The totality of the language of each claim provides additional reasons for the allowance of each claim.

Dated: February 24, 2005

Respectfully submitted,

By 

Thomas J. D'Amico

Registration No.: 28,371

DICKSTEIN SHAPIRO MORIN &
OSHINSKY LLP

2101 L Street NW

Washington, DC 20037-1526

(202) 785-9700